MULTIPLE WIDTH TRANSCEIVER HOST BOARD SYSTEM

ABSTRACT OF THE DISCLOSURE

The present invention provides a host board system in which transceivers of two sizes (the larger approximately twice the width of the smaller) can be arbitrarily mixed within a given host board design. This is accomplished by specifying an arrangement of electrical connectors, a guide rail design, a set of transceiver features, and a bezel configuration to meet this need as well as the other requirements of optoelectronic transceivers. Typically, two slots and connectors are lined up behind an opening in a bezel that provides transceiver access to two connectors. So that either double-width or single-width transceivers can be used in the same opening, the double-width transceiver is designed to engage with the connectors in the same position as a single-width transceiver. Further, the slots and connectors are spaced evenly so that all of the slots and connectors can accommodate a single-width transceiver and all adjacent slots and connectors can accommodate a double-width transceiver.

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